HASH AND ROUTE HARDWARE WITH PARALLEL ROUTING SCHEME

Laurent Moll

Barton J. Sano

Thomas Albert Petersen

ABSTRACT OF THE DISCLOSURE

[0123] A multiprocessor switching device substantially implemented on a single CMOS integrated circuit is described in connection with a parallel routing scheme for calculating routing information for incoming packets. Using the programmable hash and route routing scheme, a hash and route circuit can be programmed for a variety of applications, such as routing, flow-splitting or load balancing.